

-1-

Image 2811.
PATENT
\$

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Peter J. Hopper et al. (AS AMENDED)

Application No.: 10/647,604

Filed: August 25, 2003

For: ULTRA LOW LEAKAGE MOSFET
TRANSISTOR

Group Art Unit: 2811

Examiner: Unknown

**PETITION TO CHANGE
INVENTORSHIP UNDER 35 C.F.R. 1.48(a)**

121 Spear Street, Suite 290
San Francisco, CA 94105
(415) 512-1312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited
with the United States Postal Service as First Class Mail in an
envelope, addressed to: Commissioner for Patents, P.O.
Box 1450, Alexandria, VA 22313-1450 on December 19, 2003
STALLMAN & POLLOCK LLP

Dated: 12/19/03 By: [Signature]
LANA T. BRENNER

Sir:

Pursuant to 37 C.F.R. 1.48(a), petition is hereby made to correct inventorship, and more specifically, to add the name of Robert Drury as a co-inventor. As noted in the attached Verified Statement of Facts by Mr. Drury, the failure to name Mr. Drury as an inventor in the original filing was an error made without deceptive intent.

In compliance with the requirements of 37 C.F.R. 1.48(a), enclosed herewith are:

1. Verified Statement of Facts by Inventor Pursuant to 37 C.F.R. 1.48(a);
2. A Declaration under 37 C.F.R. 1.63 listing the actual inventors;
3. A check in the amount of \$130.00 to cover the fee set forth in 37 C.F.R. 1.17(i);

and

4. The written consent of the Assignee, National Semiconductor Corporation.

12/29/2003 SDENBOB1 00000109 10647604
01 FC:1460 130.00 OP

Atty Docket No.: NSC1-M3000 [P05657]

The Commissioner is hereby authorized to charge payment of any fees associated with the communication or credit any overpayment to Deposit Account No. 50-1703. **A duplicate copy of this sheet is enclosed.**

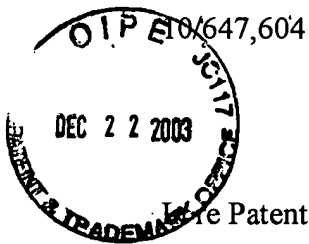
Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: 12 - 19 - 2003

By: Michael J. Pollock
Michael J. Pollock
Reg. No. 29,098

Attorneys for Applicant(s)



PATENT

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of

Peter J. Hopper et al. (AS AMENDED)

Application No.: 10/647,604

Filed: August 25, 2003

For: ULTRA LOW LEAKAGE MOSFET
TRANSISTOR

Group Art Unit: 2811

Examiner: Unknown

**WRITTEN CONSENT OF ASSIGNEE
PURSUANT TO 37 C.F.R. 1.48(a)**

121 Spear Street, Suite 290
San Francisco, CA 94105
(415) 512-1312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited
with the United States Postal Service as First Class Mail in an
envelope, addressed to: Commissioner for Patents, P. O.
Box 1450, Alexandria, VA 22313-1450 on December 19, 2003
STALLMAN & POLLOCK LLP

Dated: 12/19/03 By: [Signature]
LANA T. BRENNER

Sir:

National Semiconductor Corporation, the Assignee in the above-identified application as
set forth in the Assignment attached hereto, hereby consents to the amendment of inventorship
when Mr. Robert Drury is added as the co-inventor of the subject application.

NATIONAL SEMICONDUCTOR CORPORATION

Dated: 12/16/03

By: [Signature]
Christopher J. Byrne
Director of Intellectual Property and
Technology Licensing



10/647,604

PATENT

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Peter J. Hopper et al. (AS AMENDED)

Application No.: 10/647,604

Filed: August 25, 2003

For: ULTRA LOW LEAKAGE MOSFET
TRANSISTOR

Group Art Unit: 2811

Examiner: Unknown

**VERIFIED STATEMENT OF FACTS BY
INVENTOR PURSUANT TO 37 C.F.R.
1.48(a)**

121 Spear Street, Suite 290
San Francisco, CA 94105
(415) 512-1312

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on December 19, 2003
STALLMAN & POLLOCK LLP

Dated: 12/19/03

By:

LANA T. BRENNER

Sir:

I, Robert Drury, declare as follows:

1. I believe there was an error in the inventorship of the above-identified application as originally filed and that my name should now be added as a co-inventor in this application.

2. The error in inventorship occurred without any deceptive intention on my part.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Dated:

12/16/03

Respectfully submitted,

By:

Robert Drury

Atty Docket No.: NSC1-M3000 [P05657]



Atty Docket No. NSC1-M3000 [P05657]

SUPPLEMENTAL DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

ULTRA LOW LEAKAGE MOSFET TRANSISTOR

the specification of which was filed on August 25, 2003, as Application No. 10/647,604 and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

| Prior Foreign Application(s) | | | Priority Claimed | |
|------------------------------|---------|----------------------|--------------------------|--------------------------|
| | | | <u>Yes</u> | <u>No</u> |
| _____ | _____ | _____ | <input type="checkbox"/> | <input type="checkbox"/> |
| Number | Country | Day/Month/Year Filed | | |
| _____ | _____ | _____ | <input type="checkbox"/> | <input type="checkbox"/> |
| Number | Country | Day/Month/Year Filed | | |

I hereby claim the benefit under 35 U.S.C. § 119(3) of any United States provisional application(s) below:

| | |
|--------------------|-------------|
| _____ | _____ |
| Application Number | Filing Date |
| _____ | _____ |
| Application Number | Filing Date |
| _____ | _____ |
| Application Number | Filing Date |

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C. § 112, I acknowledge the duty to disclose all information which is material to patentability as defined in 37 CFR § 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

| Application Number | Filing Date | Status: Patented, Pending, Abandoned |
|--------------------|-------------|--------------------------------------|
|--------------------|-------------|--------------------------------------|

| Application Number | Filing Date | Status: Patented, Pending, Abandoned |
|--------------------|-------------|--------------------------------------|
|--------------------|-------------|--------------------------------------|

| Application Number | Filing Date | Status: Patented, Pending, Abandoned |
|--------------------|-------------|--------------------------------------|
|--------------------|-------------|--------------------------------------|

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under 18 U.S.C. § 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor Peter J. Hopper

Inventor's signature



12.16.03

Date

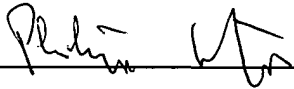
Residence 4327 Verdigris Circle, San Jose, CA 95134

Citizenship United Kingdom

Post Office Address 4327 Verdigris Circle, San Jose, CA 95134

Full name of second joint inventor, if any, Philipp Lindorfer

Inventor's signature



12/16/03

Date

Residence 6180 Cahalan Avenue, San Jose, CA 95134

Citizenship Austria

Post Office Address 6180 Cahalan Avenue, San Jose, CA 95134

Full name of third joint inventor, if any, Vladislav Vashchenko

Inventor's signature



12/16/03

Date

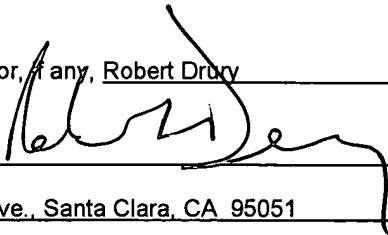
Residence 700 Paul Avenue, Palo Alto, CA 94306

Citizenship Russia

Post Office Address 700 Paul Avenue, Palo Alto, CA 94306

Full name of fourth joint inventor, if any, Robert Drury

Inventor's signature



12/16/03

Date

Residence 3185 Machado Ave., Santa Clara, CA 95051

Citizenship United Kingdom

Post Office Address 3185 Machado Ave., Santa Clara, CA 95051